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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Steinmann et al

Art Unit: 1765

Serial No.: 09/852,921

Examiner: Deo, D.

Filing Date: 05/10/01

Docket No.: TJ-29881

Title: METHOD OF INTEGRATING A THIN FILM RESISTOR IN A
MULTI-LEVEL METAL TUNGSTEN-PLUG INTERCONNECT

CERTIFICATION OF FACSIMILE TRANSMISSION

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Jacqueline J. Garner, Reg. No. 36,144

March 31, 2003
Date

FACSIMILE COVER SHEET

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<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT (# Pages)																																
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<table border="1"> <tr> <td colspan="2">NAME OF INVENTOR(S): Steinmann</td> <td colspan="2">RECEIPT DATE & SERIAL NO.:</td> </tr> <tr> <td colspan="2">TITLE OF INVENTION: Method of Integrating a Thin Film Resistor . . .</td> <td colspan="2">Serial No.: 09/852,921</td> </tr> <tr> <td colspan="2"></td> <td colspan="2">Filing Date: 05/10/2110</td> </tr> <tr> <td>TITLE NO.:</td> <td>DEPOSIT ACCT. NO.:</td> <td colspan="2"></td> </tr> <tr> <td>TI-29881</td> <td>20-0668</td> <td colspan="2"></td> </tr> <tr> <td colspan="2">FAXED: 3/31/2003</td> <td colspan="2"></td> </tr> <tr> <td colspan="2">DUE: 3/31/2003</td> <td colspan="2"></td> </tr> <tr> <td colspan="2">ATTY/SEC'Y: JYG</td> <td colspan="2"></td> </tr> </table>		NAME OF INVENTOR(S): Steinmann		RECEIPT DATE & SERIAL NO.:		TITLE OF INVENTION: Method of Integrating a Thin Film Resistor . . .		Serial No.: 09/852,921				Filing Date: 05/10/2110		TITLE NO.:	DEPOSIT ACCT. NO.:			TI-29881	20-0668			FAXED: 3/31/2003				DUE: 3/31/2003				ATTY/SEC'Y: JYG			
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Texas Instruments Incorporated
P.O. Box 655474, M/S 3999
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**REQUEST
FOR
CONTINUED EXAMINATION (RCE)
TRANSMITTAL**Subsection (B) of 35 U.S.C. § 132, effective on May 29, 2000,
provides for continued examination of an utility or plant application
filed on or after June 8, 1995.
See The American Inventors Protection Act of 1999 (AIPA).

Application Number	09/852,921
Filing Date	05/10/01
First Named Inventor	Stehrman
Group Art Unit	1765
Examiner Name	Deo, D
Attorney Docket Number	TI-29881

This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application.

NOTE: 37 C.F.R. § 1.114 is effective on May 29, 2000. If the above-identified application was filed prior to May 29, 2000, applicant may wish to consider filing a continued prosecution application (CPA) under 37 C.F.R. § 1.53 (d) (PTO/SB/28) instead of a RCE to be eligible for the patent term adjustment provisions of the AIPA. See Changes to Application Examination and provisional Application Practice, Interim Rule, 65 Fed. Reg. 14505, (Mar. 20, 2000), 1233 Off. Gaz. Pat. Office 47 (Apr. 11, 2000), which established RCE practice.**1 Submission required under 37 C.F.R. § 1.114**

- a ☐ Previously submitted
- i. ☐ Consider the amendments(s)/reply under 37 C.F.R. § 1.116 previously filed on _____
(Any unentered amendments(s) referred to above will be entered).
- ii. ☐ Consider the arguments in the Appeal Brief or Reply Brief previously filed on _____
- iii. ☐ Other _____
- b ☒ Enclosed
- i. ☒ Amendment/Reply
- ii. ☐ Affidavit(s)/Declaration(s)
- iii. ☐ Information Disclosure Statement (IDS)
- iv. ☐ Other _____

2 Miscellaneous

- a ☐ Suspension of action on the above-identified application is requested under 37 C.F.R. § 1.103 (c) for a period of _____ months. (Period of suspension shall not exceed 3 months; Fee under 37 C.F.R. § 1.17 (f) required)
- b ☐ Other _____

3 Fees

The RCE fee under 37 C.F.R. § 1.17(e) is required by 37 C.F.R. § 1.114 when the RCE is filed.

- a ☒ The Director is hereby authorized to charge the following fees, or credit any overpayments, to Deposit Account No. 20-0688, Texas Instruments Incorporated.

- i. ☒ RCE fee required under 37 C.F.R. § 1.17(e)
- ii. ☐ Extension of time fee (37 C.F.R. §§ 1.136 and 1.17)
- iii. ☐ Other _____


- b ☐ Check in the amount of \$ _____ enclosed

- c ☐ Payment by credit card (Form PTO-2038 enclosed)

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT REQUIRED

Name (Print / Type)	Jacqueline J. Garner	Registration No. (Attorney / Agent)	36,144
Signature		Date	3/31/2003

CERTIFICATE OF MAILING OR TRANSMISSION

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner For Patents, Box RCE, Washington, DC 20231, or facsimile transmitted to the U.S. Patent and Trademark Office on:			
Name (Print or Type)	Jacqueline J. Garner, Reg. No. 36,144	Date	3/31/2003
Signature		Date	3/31/2003

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3/1/03**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Steinmann et al Art Unit: 1785
Serial No.: 09/852,921 Examiner: Deo, D.
Filing Date: 05/10/01 Docket No.: TI-29881
Title: METHOD OF INTEGRATING A THIN FILM RESISTOR IN A
 MULTI-LEVEL METAL TUNGSTEN-PLUG INTERCONNECT

Amendment

Assistant Commissioner of Patents
Washington, DC 20231

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated 12/31/2002. They are respectfully submitted as a full and complete response to that Action.

Please amend the above-referenced application as follows:

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Amendments to the Specification

None.